

**Silicon NPN Power Transistors**

**2SC1454**

**DESCRIPTION**

- With TO-3 package
- High breakdown voltage: $V_{CE0}=250V(\text{min})$

**APPLICATIONS**

- For use in low frequency power amplifier applications

**PINNING(see Fig.2)**

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector

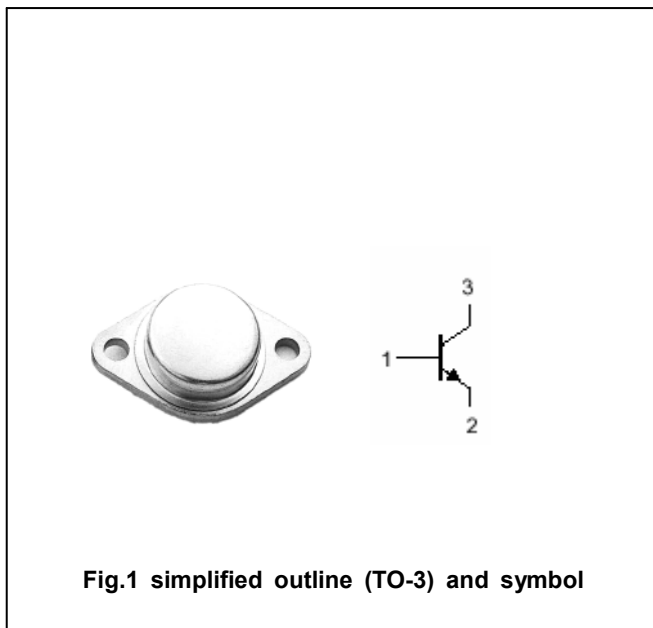


Fig.1 simplified outline (TO-3) and symbol

**Absolute maximum ratings( $T_a=\square$ )**

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$V_{CBO}$	Collector-base voltage	Open emitter	300	V
$V_{CEO}$	Collector-emitter voltage	Open base	250	V
$V_{EBO}$	Emitter-base voltage	Open collector	7	V
$I_C$	Collector current		4	A
$P_C$	Collector power dissipation	$T_C=25\square$	50	W
$T_j$	Junction temperature		150	$\square$
$T_{stg}$	Storage temperature		-55~150	$\square$

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## 2SC1454

## CHARACTERISTICS

T<sub>j</sub>=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-emitter breakdown voltage	I <sub>C</sub> =25mA ; I <sub>B</sub> =0	250			V
V <sub>CEsat</sub>	Collector-emitter saturation voltage	I <sub>C</sub> =3A; I <sub>B</sub> =0.3A			1.0	V
V <sub>BEsat</sub>	Base-emitter saturation voltage	I <sub>C</sub> =3A; I <sub>B</sub> =0.3A			1.5	V
I <sub>CBO</sub>	Collector cut-off current	V <sub>CB</sub> =300V; I <sub>E</sub> =0			0.1	mA
I <sub>EBO</sub>	Emitter cut-off current	V <sub>EB</sub> =7V; I <sub>C</sub> =0			0.1	mA
h <sub>FE</sub>	DC current gain	I <sub>C</sub> =1A ; V <sub>CE</sub> =5V	20			
f <sub>T</sub>	Transition frequency	I <sub>C</sub> =0.5A ; V <sub>CE</sub> =12V		10		MHz

PACKAGE OUTLINE



Fig.2 outline dimensions (unindicated tolerance:±0.1mm)